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**chessprogramming - 6502**

6502 KIT FUNCTIONAL BLOCK DIAGRAM Notes 1. UART is software control for low speed asynchronous communication. 2. The kit provides 8-bit LCD module interfacing bus. 3. 100Hz Tick generator is for interrupt experiment. 4. Ports for display and keypad interfacing were built with discrete logic IC chips. 5. Memory and Port decoders are made with Programmable Logic Device, PLD.

**6502 Microprocessor Kit User's Manual - kswichit.com**

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